

## JOB DESCRIPTION

### **JOB DUTIES:**

The Electrical Engineer is responsible for integrated circuit design activities for many different applications at AyDeeKay. The design activity includes but is not limited to specification, circuit design, simulations, layout, device modeling, prototype testing, manufacturability evaluation and support of quality and application engineers.

### **REQUIRED QUALIFICATIONS:**

- PhD in Electrical Engineering with focus on RFIC design.
- Minimum 2 years industrial experience in the hands-on design of a commercial RFIC product.
- Minimum 2 years experience with fully programmable fractional-N CMOS RF frequency synthesizer.
- Minimum 2 years experience of CMOS circuits (design, simulation, layout and lab measurements) including PLL, VCO, frequency divider, crystal oscillator, band gap, PFD.
- Demonstrated Knowledge of integrated circuit design flow in Mentor and/or Cadence EDA tools including schematic capture, layout, simulation, and Caliber LVS, DRC and parasitic extraction.
- Demonstrated Knowledge of device physics, simulation model parameters and semiconductor fabrication process;
- Demonstrated knowledge with top-level (mixed-mode) simulation as a final IC verification before the tape-out.

### **Work Location:**

AyDeeKay  
32451 Golden Lantern, #302  
Laguna Niguel, California 92677

### **Send Resume to:**

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Or Fax +1 949 608 0854